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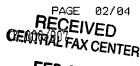
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POWER OF ATTORNEY

MULLER & HOFFMANN

FAX Nr.:+49 89 48901044

ATTORNEY DOCKET NO.



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## POWER OF ATTORNEY

Method for Patterning Dielectric Layers on Sen	niconductor Substrates	
Application Serial No. 10/724,141, filed on December 1, 2003, I hereby appoint the followattorney listed below to prosecute this application and transact all business in the Patent Trademark Office connected therewith.  All practitioners at Customer No. 48154		
Scool Correspondence to:	Direct Telephone Calls To:	
•	ira S. Matsii	
Customer No. 48154 Statur & Matril, L.L.P.	972-732-1001	
17950 Preston Rd.	\1= .~~ b	
Snite 1600		
Daulas, TX 75252-5793		
Neme of Inventor <u>Orit Schwalbe</u> Residence: <u>Ampthurger Str. 79. Dresden.</u> 01277 Germany  Post Office Atidress: Same	Citizenship: Germany	
8. Solvaha	01/26/2005	
Inventor's Signature	Date	
Name of Inventor Thomas Roder	Citizenship: Genulant	
Residence: <u>Schoenfelder Str. 15A. Dresden, 01099 Garmany</u>	<u></u>	
Post Office Address: Saint		
· ·		

SLATER & MATSIL LLP

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MULLER & HOFFMANN

FAX Nt.:+49 89 48901044

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## POWER OF ATTORNEY

POWER OF ATTORNEY	ATTORNEY DOCKET NO.	INF-119
TONDE OF STANKING	AXIOATIMA POCIDUI IIO	Marin Mar
FOR PATENT APPLICATION		

As a below named inventor for the PATENT APPLICATION entitled:

Method for Patterning Diclectric Layers on Semiconductor Substrates

Application Serial No. 10/724,141, filed on December 1, 2003, I hereby appoint the following attorney listed below to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

All practitioners at Customer No. 48154

	·
Send Correspondence to:	Direct Telephone Calls To:
Customer No. 48154	Ira S. Matell
Stater & Matyli, L.L.P. 17950 Freston Rd.	972-733-1001
State. 1090	
Dallas, TX 75252-5793	
Name of Inventor Gett Schwalbe	Chizenship: Germany
Residence: Augeburger Str. 79, Dresiden, 01277, Germany	
Post Office Address: Same	
· '	·
Inventor's Signature	Date
Name of Inventor Thomas Ruder	Cincenship: Germany
Benidence Schoolfelder Str. 15A. Dresdes, 01099 Germany	
Post Office Address: Same	
To fall	Jan 7716, 2005
Inventor's Signature	Day